		Application/Control No.	Applicant(s)/Patent Under Reexamination
Notice of References Cited		10/694,302	LARSON ET AL.
		Examiner	Art Unit	
		Lev I. Iwashko	2186	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Chavarria-Miranda, Daniel and John Mellor-Crummey, "An Evaluation of Data-Parallel Compiler Support for Line-Sweep Applications". April 2003, Journal of Instruction-Level Parallelism 5, pp. 17-28.
	V	Cray T3ETM Fortran Optimization Guide - 004-2518-002, "Chapter 4. Single-PE Optimization - Section 4.3: Optimization for Cache", January 1999, Sections 4.3.0-4.3.3.
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.